



COOL Chips VIII

CALL FOR PARTICIPATION

COOL Chips is an International Symposium initiated in 1998 to present advancement of low-power and high-speed chips. The symposium covers leading-edge technologies in all areas of microprocessors and their applications.

COOL Chips VIII is to be held in Yokohama on April 20-22, 2005, and is targeted at the architecture, design and implementation of chips with special emphasis on the areas listed below.

- **Low Power-High Performance Processors for Multimedia, Digital Consumer Electronics, Mobile, Graphics, Encryption, Robotics, Networking and Biometrics.**
- **Novel Architectures and Schemes for Single Core, Multi-Core, Embedded System, Reconfigurable Computing, Grid, Ubiquitous, Dependable Computing and Wireless.**
- **Cool Software including Binary Translations, Compiler Issues and Low Power Techniques.**

Date and Location

April 20-22, 2005

Yokohama Joho Bunka Center, Yokohama, Japan
(Yokohama Media & Communications Center, Yokohama, Japan)

Keynote Presentations

- “Microprocessors' Third Wave:
The Business and Technology Impact of Configurable
Cores on System-On-Chip Design”
Chris Rowen (Tensilica Inc.)
- “The Cell Processor and the Future”
Masakazu Suzuoki (Sony Computer Entertainment Inc.)

Invited Presentations

- “System-on-Chip Design Verification Strategies”
Chong-Min Kyung
(Korea Advanced Institute of Science and Technology)
- “Multimedia Processor-based Platform
for a Wide Range of Digital Consumer Electronics”
Tokuzo Kiyohara
(Matsushita Electric Industrial Co., Ltd.)
- “All about the Cell Processor”
Peter Hofstee (IBM Austin)

Panel Discussion

- “Will the Future Processors and the Target Market still
be Driven by the Moore's Law?”

Special Session

- “title: TBD”
David Albonesi (Cornell University)
- “title: TBD”
David Brooks (Harvard University)

(As of January 19, 2005)

Sponsored by the Technical Committees
on Microprocessors and Microcomputers
and Computer Architecture of the IEEE
Computer Society. In cooperation with
the IEICE Electronics Society, ACM
SIGARCH and IPSJ.



Symposium Registration

In order to make a registration, please use COOL Chips VIII
REGISTRATION FORM on web site at

< <http://www.coolchips.org> >

REGISTRATION FEES

Registration Fees include all plenary and technical sessions
and special invited lectures presented 4/20/05 through
4/22/05, a copy of the proceedings (copies of speakers'
slides).

	Early Registration by March 25, '05	Late Registration from March 26, '05
Member of any of IEEE IEICE, IPSJ or ACM	30,000 yen	36,000 yen
Non-Member	38,000 yen	46,000 yen
Student (Member)	10,000 yen	12,000 yen
Student (Non-Member)	13,000 yen	16,000 yen

PAYMENT AND REMITTANCE

Fees should be paid with a credit card or a bank transfer.
Credit Cards (VISA, MASTERCARD and AMERICAN
EXPRESS) are acceptable. Please indicate the account
number of your credit card and expiration date on the
Registration Form. Especially from inside Japan, bank
transfer is also acceptable.

Notes:

1. Credit card charges will be billed in Yen.
2. Personal checks, bank drafts, and traveler's checks are not acceptable.
3. Registration is incomplete without payment of the registration fee.

Accommodations

HOTEL RESERVATIONS

A special group rate is available for COOL Chips VIII attendees for
The Yokohama Novotel and Hotel New Grand.

CONTACT

IEICE (The Institute of the Electronics, Information and
Communication Engineers)

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