IEEE Symposium on Low-Power and High-Speed Chips

COOL Chips XIV

CALL FOR PARTICIPATION

COOL Chips is an International Symposium initiated in 1998 to present advancement of low-power and high-speed chips. The symposium covers leading-edge technologies in all areas of microprocessors and their applications. The COOL Chips XIV is to be held in Yokohama on April 20-22, 2011, and is targeted at the architecture, design and implementation of chips with special emphasis on the areas listed below.

- Cool Software including - Parallel Schedulers, Embedded Real-time Operating System, Binary Translations, Compiler Issues and Low Power Techniques.

**Dates and Location**
April 20-22, 2011
Yokohama Joho Bunka Center, Yokohama, Japan
(Yokohama Media & Communications Center, Yokohama, Japan)

**Keynote Presentations**
- "The Truths and Myths of Embedded Computing", Shekhar Borkar (Intel, USA)
- "AMD's Zacate, Low Power Fusion APU", Denis Foley (Advanced Micro Devices, USA)
- "Worldwide RMC Mobile Platform Solution", Juha M. Heikkilä (Renesas Mobile Corp., Finland)
- "From Multi-Core CPU to Many-Core GPU", Toru Baji (NVIDIA Japan, Japan)
- title-TBD, Patrick Lysaght (Xilinx, USA)

**Invited Presentations**
- "The Correspondence between Architecture and Application for High Speed Vision Chips", Masatoshi Ishikawa (Tokyo Univ., Japan)
- "Toward Machine Vision Technology Overcoming the Pixel Resolution Limit, -- From 3D vision to medical imaging --", Takafumi Aoki (Tohoku Univ., Japan)
- "Full Software Implementation of Real-time ISDB-T Modulator on Dynamically Reconfigurable SoC Using Practical Co-design Environment", Toru Awashima (Renesas Electronics Corp., Japan)

**Panel Discussion**
- Topics: Impact on society by fusion and harmony of mobile devices, servers, and networks -- Their direction of evolutions and optimal roles --
  Organizer/Modelator: Masato Motomura (NEC, Japan)

**Special Sessions (invited lectures)**
- "Trusted MpSoC Platforms for Safety Related Applications", Rolf Ernst (Technische Universität Braunschweig, Germany)
- "Power Measurement, Characterization and Estimation of Microprocessor-Based Systems", Naehyuck Chang (Seoul National University, Korea)

For more information, please visit <http://www.coolchips.org/>.