COOL Chips XVI
CALL FOR PARTICIPATION

COOL Chips is an International Symposium initiated in 1998 to present advancement of low-power and high-speed chips. The symposium covers leading-edge technologies in all areas of microprocessors and their applications. The COOL Chips XVI is to be held in Yokohama on April 17-19, 2013, and is targeted at the architecture, design and implementation of chips with special emphasis on the areas listed below.

- **Novel Architectures and Schemes for Single Core, Multi-Core, Embedded Systems, Reconfigurable Computing, Grid, Ubiquitous, Dependable Computing, GALS and Wireless.**
- **Cool Software including - Parallel Schedulers, Embedded Real-time Operating System, Binary Translations, Compiler Issues and Low Power Techniques.**

**Dates and Location**
April 17-19, 2013
Yokohama Joho Bunka Center, Yokohama, Japan
(Yokohama Media & Communications Center, Yokohama, Japan)

**Keynote Presentations**
- "CoolChips at the Core of a Healthier World", Bert Gyselinckx (IMEC/Holst Centre, Netherlands)
- "Why and How “Watson” Answered Questions on the TV Quiz Show?", Hiroshi Kanayama (IBM Japan, Japan)
- "What Can Supercomputers Learn from Phones?", Michael McCool (Intel, USA)
- "Next Generation Vector Supercomputer for Providing Higher Sustained Performance", Shintaro Momose (NEC, Japan)

**Invited Presentations**
- "A 28nm HKMG Single-Chip Communications Processor with 1.5GHz Dual-Core Application Processor and LTE/HSPA+ Capable Baseband Processor", Takeshi Kataoka (Renesas Mobile Corp., Japan)
- "Zero Overhead State-Retention Power-Gating and Gate-Bias on a Dual-Core ARM Cortex-A5MP Processor for 50-80% Idle Power Reduction" James Myers (ARM Ltd, UK)
- "SPARC64™ X: Fujitsu's New Generation 16 Core Processor for UNIX servers", Toshio Yoshida (Fujitsu Limited, Japan)

**Panel Discussion**
- Topics: (TBD)
- Organizer/Moderator: Yoshio Masubuchi (Toshiba, Japan)

**Special Sessions (invited lectures)**
- "STHORM: A Multi-Processor Platform and Programming Environment", Pierre Paulin (STMicroelectronics, Canada)
- "Hot Research Issues in Main Memory Subsystem", Jung Ho Ahn (Seoul National University, Korea), Sungjoo Yoo (POSTECH, Korea)

**Accommodations**
- **HOTEL RESERVATIONS**
  A special group rate is available for COOL Chips XVI attendees for Hotel Monterey Yokohama and Hotel New Grand.
- **CONTACT**
  COOL Chips XVI Organizing Committee Secretaries
  E-mail: cool_XVI@coolchips.org  http://www.coolchips.org/

**Symposium Registration**
In order to make a registration, please visit COOL Chips XVI web site:  <http://www.coolchips.org/>

**REGISTRATION FEES**
Registration Fees include a copy of the proceedings (copies of speakers' slides) of all plenary and technical sessions and special sessions presented on April 17-19, 2013.

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<th>Early Registration by April 3, '13</th>
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<td>Member of any of IEEE</td>
<td>40,000 yen</td>
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**PAYMENT**
- On-site cash payment
- Credit cards
  -- VISA/Master Cards are only acceptable
  Detailed information is available on the web site.

**Notes:**
1. Credit card charges will be billed in Yen.
2. Personal checks, bank drafts, and traveler's checks are not acceptable.

For more information, please visit <http://www.coolchips.org/>