

# COOL Chips 23

Baba Memorial Hall, Hitachi Central Research Laboratory,  
Kokubunji, Tokyo, Japan  
April 15 - 17, 2020

## CALL FOR CONTRIBUTIONS

COOL Chips is an International Symposium initiated in 1998 to present advancement of low-power and high-speed chips and systems. The symposium covers leading-edge technologies in all areas of microprocessors and their applications. The COOL Chips 23 is to be held in Tokyo on April 15-17, 2020, and is targeted at the architecture, design and implementation of chips and systems with special emphasis on the areas listed below. All papers are published online via IEEE Xplore. Especially, selected papers are encouraged to submit to the IEEE Micro and the special section in the IEICE Transactions on Electronics.

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### Contributions are solicited in the following areas:

- **Low Power-High Performance Processors and Systems for - AI, IoT, Multimedia, Digital Consumer Electronics, Mobile, Graphics, Encryption, Robotics, Automotive, Networking, Medical, Healthcare, and Biometrics.**
- **Novel Architectures and Schemes for - Single Core, Multi/Many-Core, NoC, Embedded Systems, Reconfigurable Computing, Grid, Ubiquitous, Dependable Computing, GALS and 3D Integration**
- **Cool Software including - Parallel Schedulers, Embedded Real-time Operating System, Binary Translations, Compiler Issues and Low Power Application Techniques.**

Proposals should consist of a title, an extended abstract (up to 3 pages) describing the product or topic to be presented and the name, job title, address, phone number, FAX number, and e-mail address of the presenter. The status of the product or topic should precisely be described. If this is a not-yet-announced product, and you would like to keep the submission confidential, please indicate it. We will do our best to maintain confidentiality. Proposals will be selected by the program committee's evaluation of interest to the audience. Submission should be made through website. Detailed instructions are in author's kit obtained from

<https://www.coolchips.org/>.

**Author Schedule: February 7, 2020 Extended Abstract Submission (through website)**  
**March 13, 2020 Acceptance Notified (by e-mail)**  
**March 27, 2020 Final Manuscript Submission**

You are also invited to submit proposals for poster sessions. Submission should be made through website. Detailed instructions are in author's kit obtained from

<https://www.coolchips.org/>.

**Author Schedule: March 20, 2020 Poster Abstract Submission (through website)**  
**March 23, 2020 Poster Acceptance Notified (by e-mail)**

For more information, please visit <<https://www.coolchips.org/>>.

For any questions, please contact the Secretariat <[cool\\_23@coolchips.org](mailto:cool_23@coolchips.org)>.

Sponsored by the Technical Committees on Microprocessors and Microcomputers and Computer Architecture of the IEEE Computer Society. (approval pending) In cooperation with the IEICE Electronics Society and IPSJ. (approval pending)



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(basically in alphabetical order) (As of August 28, 2019)