IEEE ⬥computer society

COOL Chips 24
CALL FOR PARTICIPATION

COOL Chips is an International Symposium initiated in 1998 to present advancement of low-power and high-speed chips and systems. The symposium covers leading-edge technologies in all areas of microprocessors and their applications. The COOL Chips 24 is to be held in Tokyo on April 14-16, 2021, and is targeted at the architecture, design and implementation of chips with special emphasis on the areas listed below.

- **Low Power-High Performance Processors and Systems for AI, IoT, Multimedia, Digital Consumer Electronics, Mobile, Graphics, Encryption, Robotics, Automotive, Networking, Medical, Healthcare, and Biometrics.**
- **Novel Architectures and Schemes for Single Core, Multi-Core, Embedded Systems, Reconfigurable Computing, Grid, Ubiquitous, Dependable Computing, GALS and Wireless.**
- **Cool Software including - Parallel Schedulers, Embedded Real-time Operating System, Binary Translations, Compiler Issues and Low Power Application Techniques.**

Dates of Virtual Symposium

April 14 (Wed) – 16 (Fri), 2021

Due to the uncertain situation with COVID-19, the organizing committee has decided to give up holding the conference normally at the original venue and have the COOL Chips 24 as in a web fashion.
We appreciate your understanding and apologize to all of you for the inconvenience.

Keynote Presentations

- "虎穴に入らずんば虎子を得ず<High Risk, high return /No Risk, no return>: Domain-specific Processors make for Cool Solutions", **Avi Baum** *(Hailo)*

- "Why Preferred Networks Made MN-Core?", **Yusuke Doi** *(Preferred Networks)*

- "Codesign and System of the Supercomputer "Fugaku" ", **Mitsuhisa Sato** *(Riken)*

- "High-Efficiency Inferencing for Scalable Machine Learning", **Art Swift** *(Esperanto Technologies)*

Invited Presentations

- "Architectural Challenges in the Era of New Technologies and Extreme Heterogeneity", **Anastasiia Butko** *(Lawrence Berkeley Nat'l Lab.)*

- "The CMOS image sensor Advance in key technology and the Introduction of Next-generation image sensor", **Akito Kuwabara** *(Sony Semiconductor Solutions)*

Panel Discussion

- Topics: ""Hot" Techs for "Cool" AI Computing: Do We have Enough Tricks?" (tentative), *Moderator:* **Masato Motomura** *(Tokyo Tech)*

Special Session (invited lecture)

- "Reducing Errors in Quantum Computation via Program Transformation", **Moinuddin Qureshi** *(Georgia Institute of Technology)*

- "Title: TBA", **Jakub Szefer** *(Yale University)*

(in alphabetical order)

For detailed and up-to-date information, please visit
< https://www.coolchips.org/ >

Symposium Registration

In order to make a registration, please visit COOL Chips 24 web site: **< https://www.coolchips.org/ >**

== *REGISTRATION FEES* ==
Registration Fees include a copy of the proceedings (copies of speakers' slides) of all plenary and technical sessions and special sessions presented on April 14-16, 2021.

(including tax)	**Early** Registration by **April 9,** 2021	Late Registration from April 10, 2021
Member of any of IEEE, IEICE or IPSJ	30,000 yen	36,000 yen
Student (Member)	10,000 yen	12,000 yen
Life/Retired (Member)	10,000 yen	12,000 yen
Non-Member	40,000 yen	48,000 yen
Student (Non-Member)	14,000 yen	16,000 yen

== **PAYMENT** ==
Only credit cards via Whova Registration-site

Notes:
Credit card charges will be billed in Yen.

== *ONLINE CHECK-IN* ==
Check-in information will be provided severally by email from COOL Chips 24 no later than April 13.

== *CONTACT to* ==
Organizing Committee Secretaries
E-mail:cool_24@coolchips.org
https://www.coolchips.org/

(As of March 3, 2021)