



COOL Chips XV

CALL FOR PARTICIPATION

COOL Chips is an International Symposium initiated in 1998 to present advancement of low-power and high-speed chips. The symposium covers leading-edge technologies in all areas of microprocessors and their applications. The COOL Chips XV is to be held in Yokohama on April 18-20, 2012, and is targeted at the architecture, design and implementation of chips with special emphasis on the areas listed below.

- **Low Power-High Performance Processors for Multimedia, Digital Consumer Electronics, Mobile, Graphics, Encryption, Robotics, Automotive, Networking, Medical, Healthcare, and Biometrics.**
- **Novel Architectures and Schemes for Single Core, Multi-Core, Embedded Systems, Reconfigurable Computing, Grid, Ubiquitous, Dependable Computing, GALS and Wireless.**
- **Cool Software including - Parallel Schedulers, Embedded Real-time Operating System, Binary Translations, Compiler Issues and Low Power Techniques.**

Dates and Location

April 18-20, 2012

Yokohama Joho Bunka Center, Yokohama, Japan
(Yokohama Media & Communications Center, Yokohama, Japan)

Keynote Presentations

- "Tofu Interconnect Controller for Fujitsu's Highly Scalable Supercomputer",
Yuichiro Ajima (Fujitsu, Japan)
- "The IBM Blue Gene/Q Supercomputer",
George Liang-Tai Chiu (IBM, USA)
- "Nonvolatile Logic-in-Memory Architecture Using an MTJ/MOS-Hybrid Structure and Its Applications",
Takahiro Hanyu (Tohoku Univ., Japan)
- "The Expanding Universe of Embedded Imaging",
Masaki Hiraga (Morpho, Inc., Japan)
- "Application Scalability – Key to Low Power, Performance Growth, and Exascale",
Wen-mei Hwu (Illinois Univ., USA)

Special Invited Presentation

- "Seahawk - Optimizing power efficiency in high performance Cortex-A15 processor implementations",
Sumit Sahai (ARM, UK)

Panel Discussion

- Topics: "Technology exchange:
Supercomputing and Embedded computing",
Organizer/Moderator:
Hideharu Amano (Keio Univ, Japan)

Special Sessions (invited lectures)

- "The Challenges of Analyzing Embedded Processor Behavior in the Age of Complex SoCs",
Markus Levy (EEMBC, USA)
- "Advanced Virtual Prototyping of Multiprocessor SoCs", *Frédéric Pétrot (TIMA Laboratory, France)*

For more information, please visit <<http://www.coolchips.org/>>

Symposium Registration

In order to make a registration, please use COOL Chips XV REGISTRATION FORM on web site at

< <http://www.coolchips.org/> >

REGISTRATION FEES

Registration Fees include a copy of the proceedings (copies of speakers' slides) of all plenary and technical sessions and special sessions presented on April 18-20, 2012.

	Early Registration by April 4, '12	Late Registration from April 5, '12
Member of any of IEEE IEICE, IPSJ or ACM	40,000 yen	50,000 yen
Non-Member	50,000 yen	63,000 yen
Student (Member)	13,000 yen	16,000 yen
Student (Non-Member)	17,000 yen	20,000 yen

PAYMENT

For attendees excluding presenters:

- On-site cash payment is strongly recommended.
- Credit cards (VISA/MasterCard) are necessarily acceptable.

For presenters of accepted papers:

- Credit cards (VISA/MasterCard) are only acceptable.

Detailed information is available on the web site.

Notes:

1. Credit card charges will be billed in Yen.
2. Personal checks, bank drafts, and traveler's checks are not acceptable.

Accommodations

HOTEL RESERVATIONS

A special group rate is available for COOL Chips XV attendees for Hotel Monterey Yokohama and Hotel New Grand.

CONTACT to

COOL Chips XV Organizing Committee Secretaries
E-mail: cool_XV@coolchips.org <http://www.coolchips.org/>

Sponsored by the Technical Committees on
Microprocessors and Microcomputers and Computer
Architecture of the IEEE Computer Society.
In cooperation with the IEICE Electronics Society
and IPSJ.



(As of March 22, 2012)